

Refine Search

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"6230309"	2
6230309S	0
"6230309".PN..PGPB,USPT,USOC,EPAB,JPAB,DWPI,TDBD.	2
(6230309.PN.)..PGPB,USPT,USOC,EPAB,JPAB,DWPI,TDBD.	2

Database:

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 Derwent World Patents Index
 IBM Technical Disclosure Bulletins

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6266760.pn.

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DB=PGPB,USPT,USOC,EPAB,JPAB,DWPI,TDBD; PLUR=YES; OP=OR

<u>L30</u>	6230309.pn.	2	<u>L30</u>
<u>L29</u>	l26 not l27	23	<u>L29</u>
<u>L28</u>	L25 and l8	4	<u>L28</u>
<u>L27</u>	L25 and l7	26	<u>L27</u>
<u>L26</u>	L25 and l6	49	<u>L26</u>
<u>L25</u>	L24 and (application near1 specific or ASIC\$1)	240	<u>L25</u>
<u>L24</u>	L22 and (field near2 programmable or fpga\$1 or pla\$1 or pld\$1 or mcm\$1)	556	<u>L24</u>
<u>L23</u>	L22 and adapt\$6	890	<u>L23</u>
<u>L22</u>	L21 and (mux\$2 or multiplex\$6 or select\$6 or sel or cross near1 bar\$1 or	1528	<u>L22</u>

	interconnect\$5) near12 (reconfigur\$6 or configur\$5)		
	(computat\$5 or computing or computed or computes or compute or calculat\$5		
<u>L21</u>	or multipl\$6 or add\$5) near5 (plur\$7 or array or matrix or matrices or	16796	<u>L21</u>
	multipl\$6 or dual or two or second) near15 (finite or linear or bit near3 level)		
<u>L20</u>	L17 and 18	8	<u>L20</u>
<u>L19</u>	L17 and 17	29	<u>L19</u>
<u>L18</u>	L17 and 16	51	<u>L18</u>
<u>L17</u>	l13 and (switch\$6 or select\$5) near5 mode\$1	742	<u>L17</u>
<u>L16</u>	L13 and 18	28	<u>L16</u>
<u>L15</u>	L13 and 17	43	<u>L15</u>
<u>L14</u>	L13 and 16	71	<u>L14</u>
<u>L13</u>	l9 and adapt\$6	1350	<u>L13</u>
<u>L12</u>	L9 and 18	44	<u>L12</u>
<u>L11</u>	L9 and 17	49	<u>L11</u>
<u>L10</u>	L9 and 16	81	<u>L10</u>
<u>L9</u>	L5 and (application near1 specific or ASIC\$1)	1616	<u>L9</u>
	<i>DB=PGPB,USPT; PLUR=YES; OP=OR</i>		
<u>L8</u>	(716/2-21)![CCLS]	8226	<u>L8</u>
<u>L7</u>	(712/2-203, 221-229)![CCLS]	5060	<u>L7</u>
<u>L6</u>	(712/2-300)![CCLS]	11311	<u>L6</u>
	<i>DB=PGPB,USPT,USOC,EPAB,JPAB,DWPI,TDBD; PLUR=YES; OP=OR</i>		
<u>L5</u>	L3 and (mux\$2 or multiplex\$6 or select\$6 or sel or cross near1 bar\$1 or	3798	<u>L5</u>
	interconnect\$5) near12 (reconfigur\$6 or configur\$5)		
<u>L4</u>	L3 and (reconfigur\$6 or configur\$5)	13427	<u>L4</u>
<u>L3</u>	L2 and (field near2 programmable or fpga\$1 or pla\$1 or pld\$1 or mcm\$1)	17454	<u>L3</u>
<u>L2</u>	L1 and (finite or linear or bit near3 level)	47712	<u>L2</u>
	(computat\$5 or computing or computed or computes or compute or calculat\$5		
<u>L1</u>	or multipl\$6 or add\$5) near5 (plur\$7 or array or matrix or matrices or	101262	<u>L1</u>
	multipl\$6 or dual or two or second) and real near1 time		

END OF SEARCH HISTORY